

**68mΩ, 600V, Super Junction N-Channel Power MOSFET**
**SRC60R068BS**

## General Description

The Sanrise SRC60R068BS is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency.

The SRC60R068BS break down voltage is 600V and it has a high rugged avalanche characteristics. The SRC60R068BS is available in TO-247, TO-263-2, TO-220C and TO-220F packages.

## Features

- Ultra Low  $R_{DS(ON)} = 68m\Omega$  @  $V_{GS} = 10V$ .
- Ultra Low Gate Charge,  $Q_g=110nC$  typ.
- Fast switching capability
- Robust design with better EAS performance
- EMI Improved
- Non-automotive Qualified

## Application

- Telecom Power
- EV Charger
- High Power Application

## Symbol

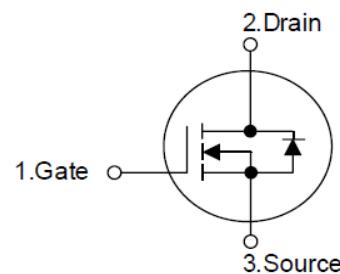


Figure 1 Symbol of SRC60R068BS

## Package Type

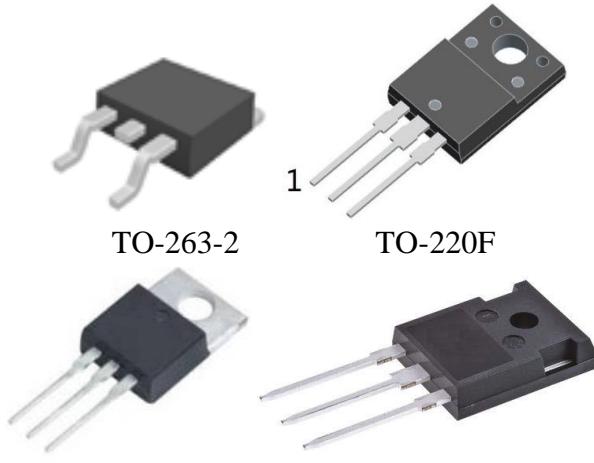
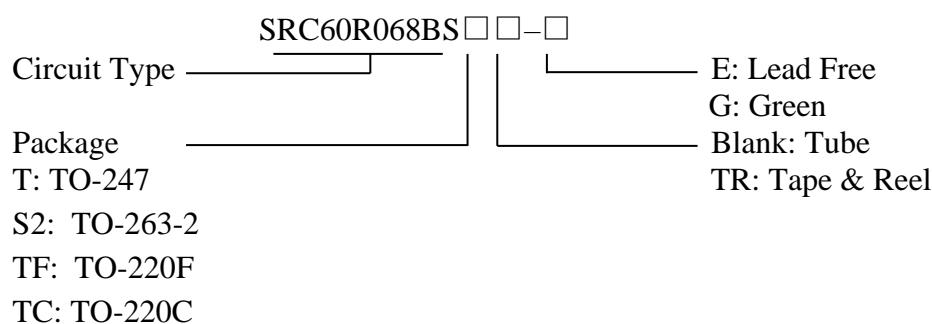


Figure 2 Package Types of SRC60R068BS

## Ordering Information



Package	Part Number		Marking ID		Packing Type
	Lead Free	Green	Lead Free	Green	
TO-247	SRC60R068BST-E	SRC60R068BST-G	SRC60R068BSTE	SRC60R068BSTG	Tube
TO-263-2	SRC60R068BSS2TR-E	SRC60R068BSS2TR-G	SRC60R068BSS2E	SRC60R068BSS2G	Tape & Reel
TO-220F	SRC60R068BSTF-E	SRC60R068BSTF-G	SRC60R068BSTFE	SRC60R068BSTFG	Tube
TO-220C	SRC60R068BSSTC-E	SRC60R068BSSTC-G	SRC60R068BSTCE	SRC60R068BSTCG	Tube

**68mΩ, 600V, Super Junction N-Channel Power MOSFET**
**SRC60R068BS**
**Absolute Maximum Ratings**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DSS</sub>	600	V
Gate-Source Voltage	V <sub>GSS</sub>	±30	V
Continuous Drain Current T <sub>C</sub> =25°C	I <sub>D</sub>	48	A
T <sub>C</sub> =125°C		21.5	
Pulsed Drain Current (Note 2)	I <sub>DM</sub>	144	A
Avalanche Energy, Single Pulse (Note 3)	E <sub>AS</sub>	610	mJ
Avalanche Energy, Repetitive (Note 2)	E <sub>AR</sub>	0.6	mJ
Avalanche Current, Repetitive (Note 2)	I <sub>AR</sub>	4.8	A
Continuous Diode Forward Current	I <sub>S</sub>	48	A
Diode Pulse Current	I <sub>S,PULSE</sub>	144	A
MOSFET dv/dt Ruggedness, V <sub>DS</sub> <=480V	dv/dt	50	V/ns
Reverse Diode dv/dt, V <sub>DS</sub> <=480V, I <sub>SD</sub> <=I <sub>D</sub>	dv/dt	50	V/ns
Power Dissipation (TO-247)	P <sub>D</sub>	357	W
ESD	HBM	>1000	V
Operating Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)	T <sub>LEAD</sub>	260	°C

Note:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. I<sub>AS</sub> = 4.8A, V<sub>DD</sub> = 60V, R<sub>G</sub> = 25Ω, Starting T<sub>J</sub> = 25°C

**Thermal characteristics**

Parameter	Symbol	Min	Typ	Max	Unit
Thermal resistance, Junction-to-Case	TO-220F	R <sub>thJC</sub>	0.35	3.5	°C /W
	TO-220C			0.35	
	TO-247			0.35	
	TO-263			0.35	
Thermal resistance, Junction-to-Ambient	TO-220F	R <sub>thJA</sub>	58	70	°C /W
	TO-220C			58	
	TO-247			58	
	TO-263			58	

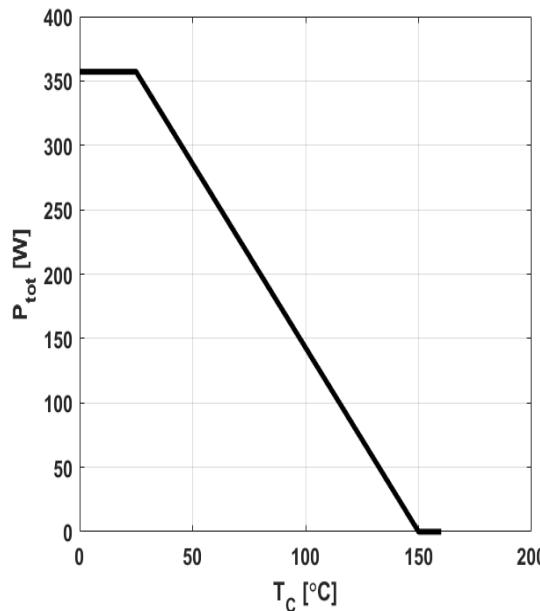
**68mΩ, 600V, Super Junction N-Channel Power MOSFET**
**SRC60R068BS**
**Electrical Characteristics**
 $T_J = 25^\circ\text{C}$ , unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Statistic Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	600			V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=600\text{V}, \text{V}_{\text{GS}}=0\text{V}$			10	$\mu\text{A}$
Gate-Body Leakage Current	Forward	$\text{I}_{\text{GSSF}}$	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		100	nA
	Reverse	$\text{I}_{\text{GSSR}}$	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-100	
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{TH})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=1.0\text{mA}$	3.0	4.0	5.0	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=24\text{A}$		57	68	$\text{m}\Omega$
Gate Resistance	$\text{R}_G$	f=1MHz, Open Drain		1.0		$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$\text{C}_{\text{ISS}}$	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1\text{MHz}$		4.3		nF
Output Capacitance	$\text{C}_{\text{OSS}}$			171		pF
Reverse Transfer Capacitance	$\text{C}_{\text{RSS}}$			2.8		pF
Effective output capacitance, energy related <sup>NOTE5</sup>	$\text{C}_{\text{O(er)}}$	$\text{V}_{\text{GS}}=0\text{V},$ $\text{V}_{\text{DS}}=0\ldots 400\text{V}$		94		pF
Effective output capacitance, time related <sup>NOTE6</sup>	$\text{C}_{\text{O(tr)}}$			550		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=400\text{V}, \text{I}_D=24\text{A}$ $\text{R}_G=3.3\Omega, \text{V}_{\text{GS}}=10\text{V}$		16		ns
Rise Time	$t_r$			6.0		
Turn-off Delay Time	$t_{\text{d}(\text{off})}$			98		
Fall Time	$t_f$			4.0		
<b>Gate Charge Characteristics</b>						
Gate to Source Charge	$\text{Q}_{\text{gs}}$	$\text{V}_{\text{DD}}=480\text{V}, \text{I}_D=24\text{A}$ $\text{V}_{\text{GS}}=0 \text{ to } 10\text{V}$		28.1		nC
Gate to Drain Charge	$\text{Q}_{\text{gd}}$			56.0		
Gate Charge Total	$\text{Q}_g$			110		
Gate Plateau Voltage	$\text{V}_{\text{plateau}}$			6.5		V
<b>Reverse Diode Characteristics</b>						
Drain-Source Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{SD}}=24\text{A}$		0.9	1.1	V
Reverse Recovery Time	$t_{\text{rr}}$	$\text{V}_R=400\text{V}, \text{I}_F=24\text{A}$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$		141		ns
Reverse Recovery Charge	$\text{Q}_{\text{rr}}$			0.83		uC
Peak Reverse Recovery Current	$\text{I}_{\text{rrm}}$			11.8		A

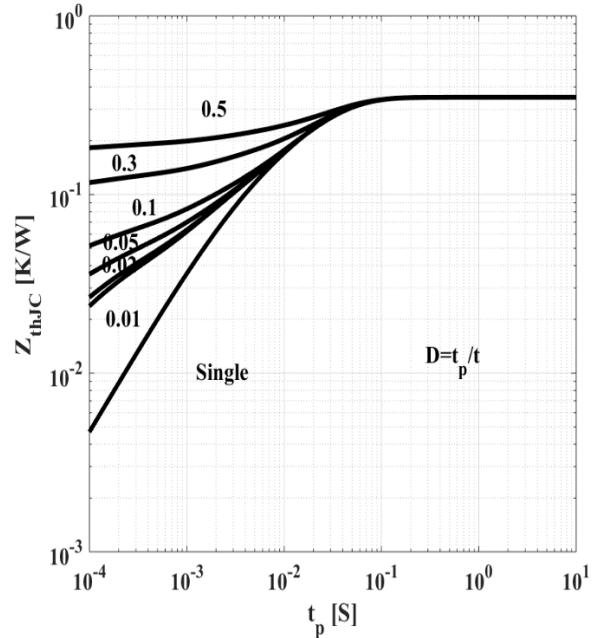
Note:

5.  $\text{C}_{\text{O(er)}}$  is a fixed capacitance that gives the same stored energy as  $\text{C}_{\text{OSS}}$  while  $\text{V}_{\text{DS}}$  is rising from 0 to 480V

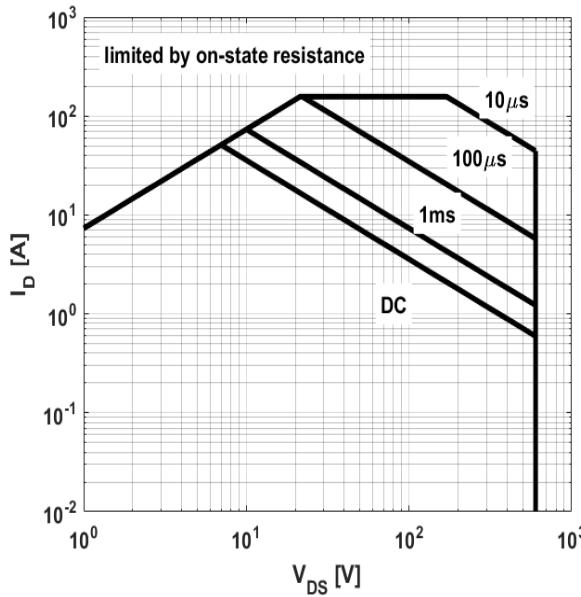
6.  $\text{C}_{\text{O(tr)}}$  is a fixed capacitance that gives the same charging time as  $\text{C}_{\text{OSS}}$  while  $\text{V}_{\text{DS}}$  is rising from 0 to 480 V

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**Typical Performance Characteristics**
**Figure 3: Power Dissipation**


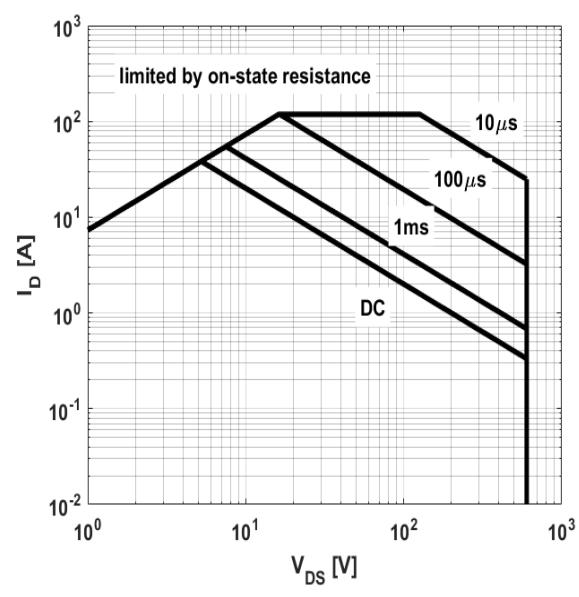
$$P_{tot} = f(T_c); \text{ TO-247}$$

**Figure 4: Max. Transient Thermal Impedance**


$$Z_{(thJC)} = f(t_p); \text{ parameter: } D = t_p/T; \text{ TO-247}$$

**Figure 5: Safe Operating Area**


$$I_D = f(V_{DS}); T_c = 25^\circ\text{C}; V_{GS} > 7\text{V}; \text{ parameter } t_p$$

**Figure 6: Safe Operating Area**


$$I_D = f(V_{DS}); T_c = 80^\circ\text{C}; V_{GS} > 7\text{V}; \text{ parameter } t_p$$



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Figure 7: Typ. Output Characteristics

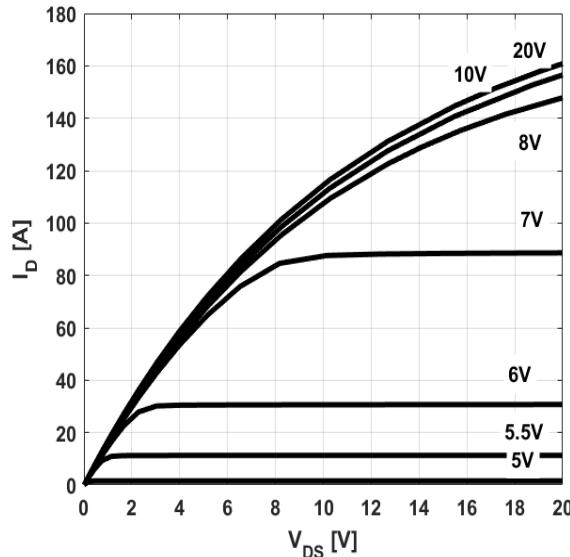
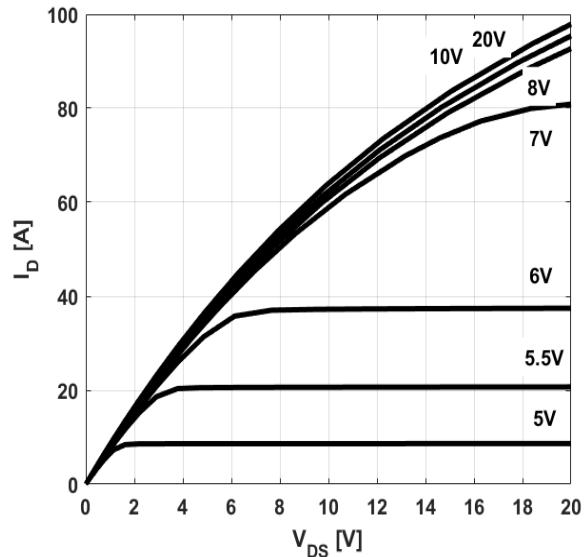


Figure 8: Typ. Output Characteristics



$I_D = f(V_{DS})$ ;  $T_j = 25^\circ\text{C}$ ; parameter:  $V_{GS}$

$I_D = f(V_{DS})$ ;  $T_j = 125^\circ\text{C}$ ; parameter:  $V_{GS}$

Figure 9: Typ. Drain-Source On-State Resistance

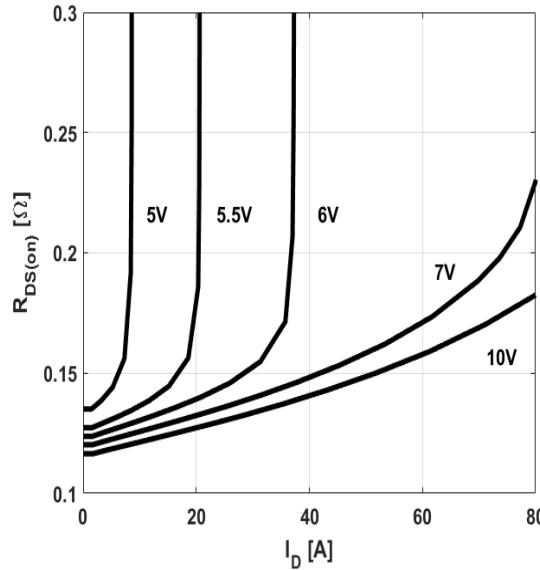
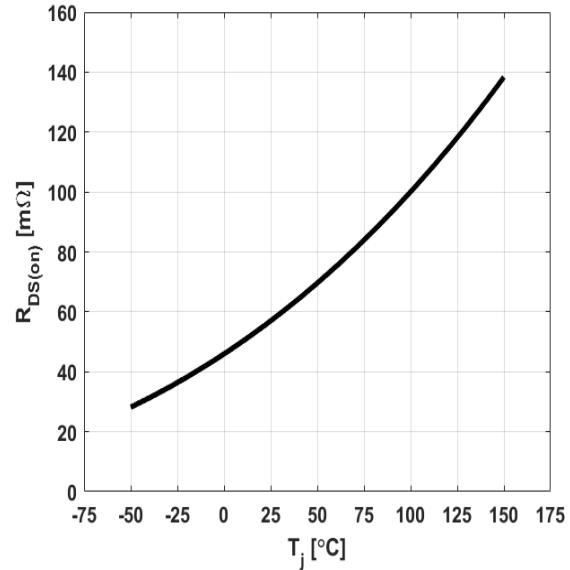


Figure 10: Typ. Drain-Source On-State Resistance



$R_{DS(ON)} = f(I_D)$ ;  $T_j = 125^\circ\text{C}$ ; parameter:  $V_{GS}$

$R_{DS(ON)} = f(T_j)$ ;  $I_D = 24\text{A}$ ;  $V_{GS} = 10\text{V}$

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Figure 11: Typ. Transfer Characteristics

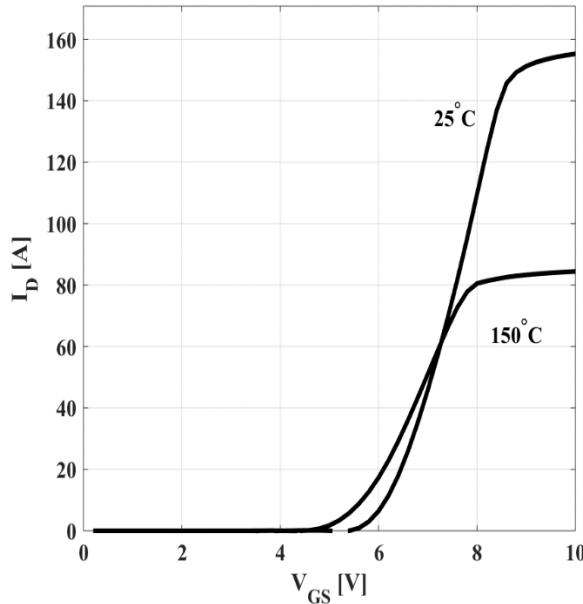

 $I_D = f(V_{GS})$ ;  $V_{DS} = 20V$ 

Figure 12: Typ. Gate Charge

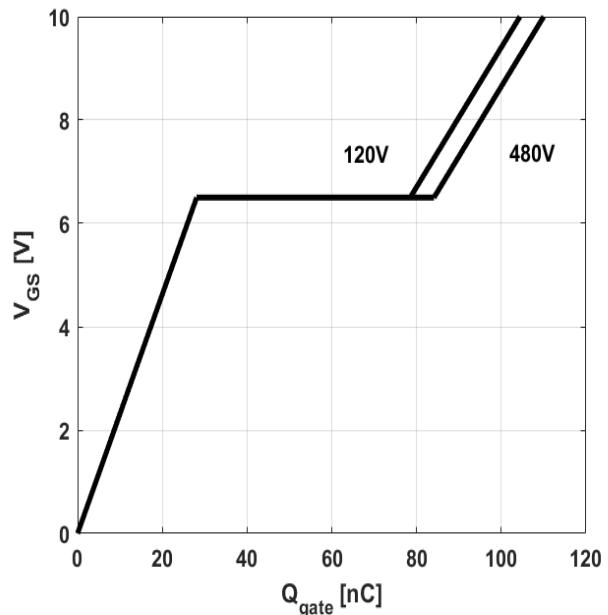

 $V_{GS} = f(Q_{gate})$ ,  $I_D = 24A$  pulsed

Figure 13: Drain-Source Breakdown Voltage

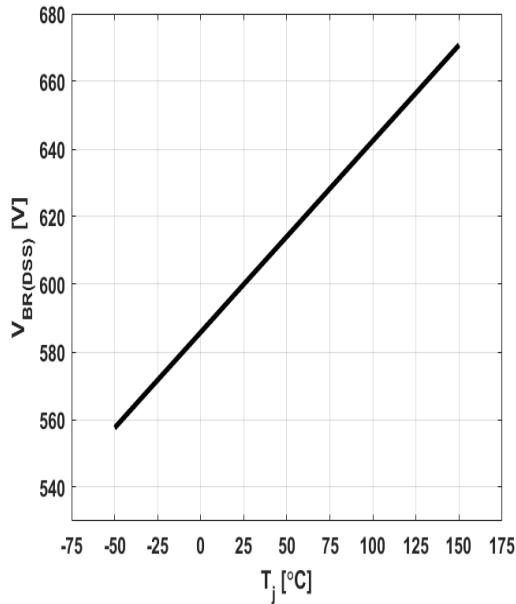
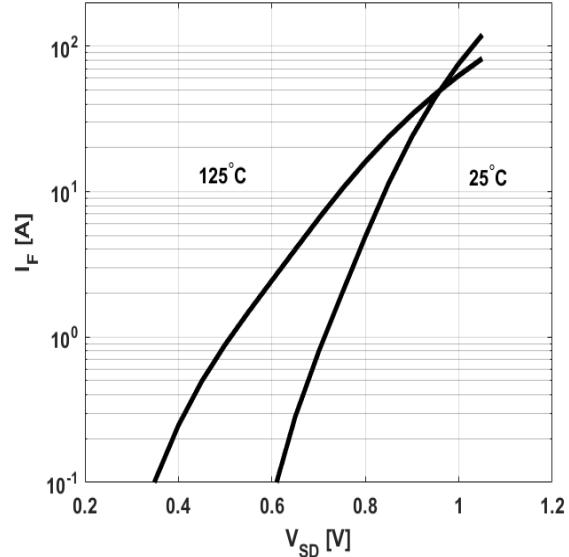
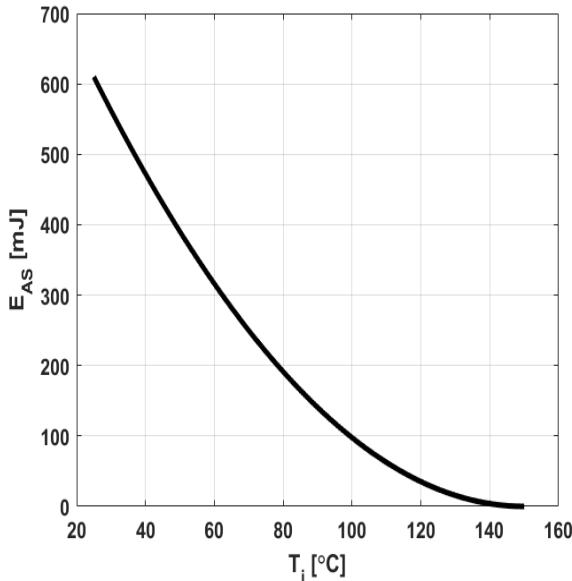
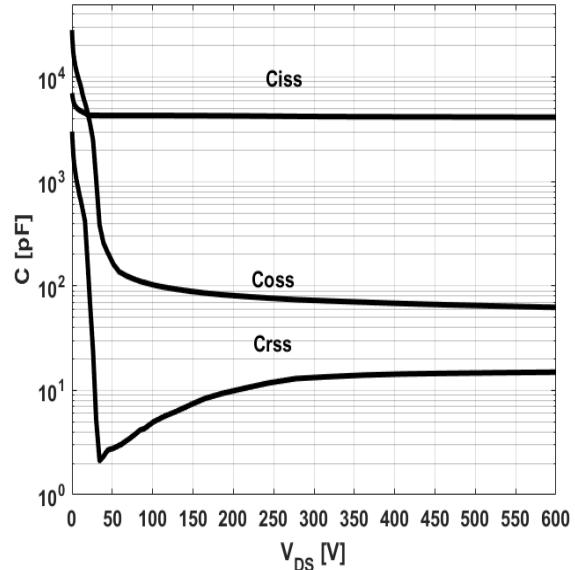

 $V_{BR(DSS)} = f(T_j)$ ;  $I_D = 1mA$ 

Figure 14: Forward Characteristics of Reverse Diode

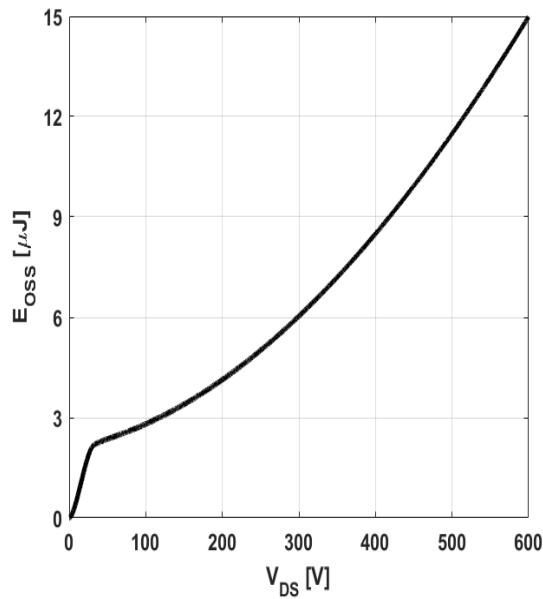

 $I_F = f(V_{SD})$ ; parameter:  $T_j$

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**Figure 15: Avalanche Energy**


$$E_{AS}=f(T_j); I_D=4.8A; V_{DD}=60V$$

**Figure 16: Typ. Capacitances**


$$C=f(V_{DS}); V_{GS}=0; f=1MHz$$

**Figure 17: Coss Stored Energy**


$$E_{oss}=f(V_{DS})$$

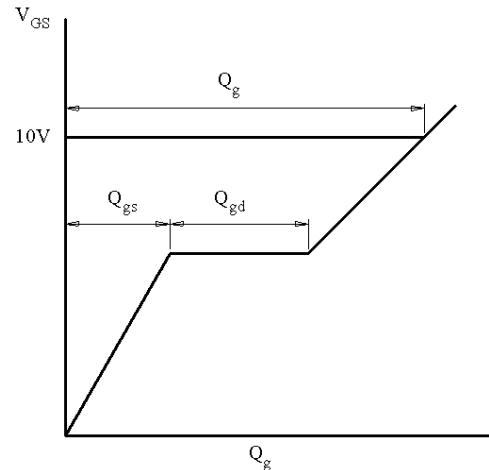
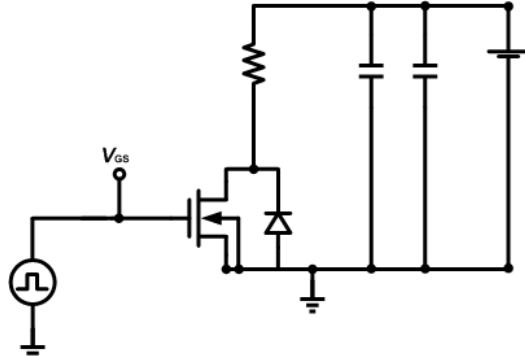


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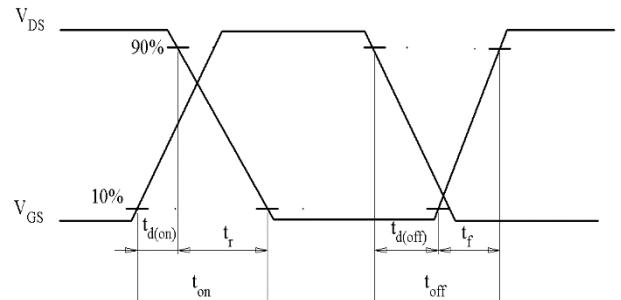
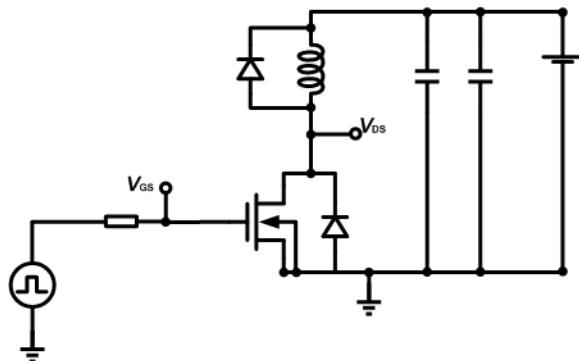
SRC60R068BS

### Test Circuits

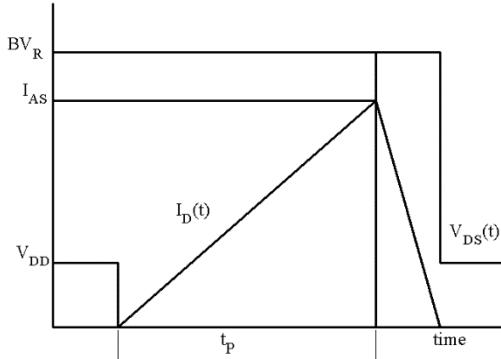
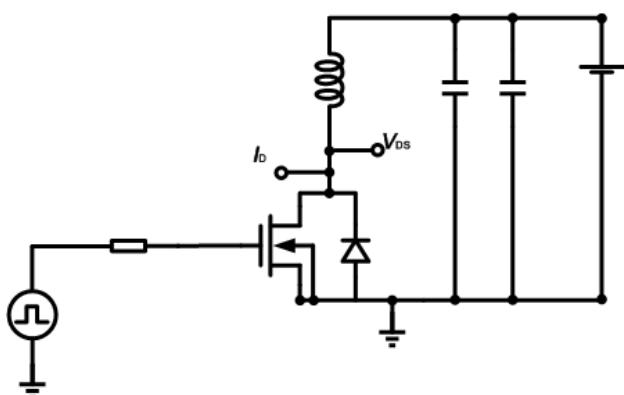
#### 1. Gate Charge Test Circuit & Waveform



#### 2. Switch Time Test Circuit

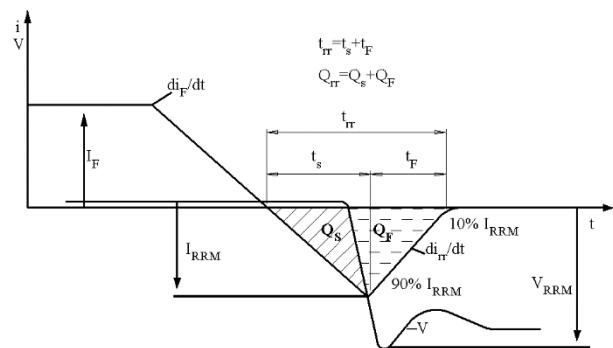
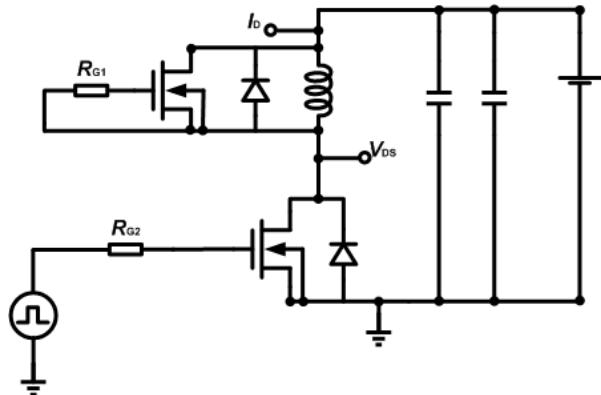


#### 3. Unclaimed Inductive Switching Test Circuit & Waveforms



**68mΩ, 600V, Super Junction N-Channel Power MOSFET**
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#### 4. Test Circuit and Waveform for Diode Characteristics





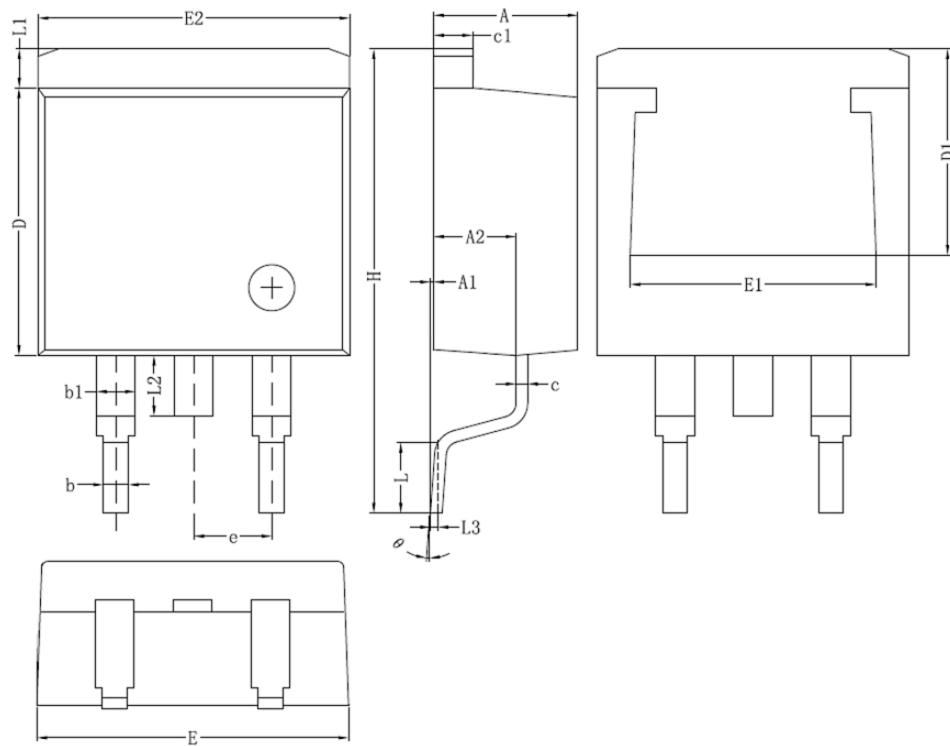
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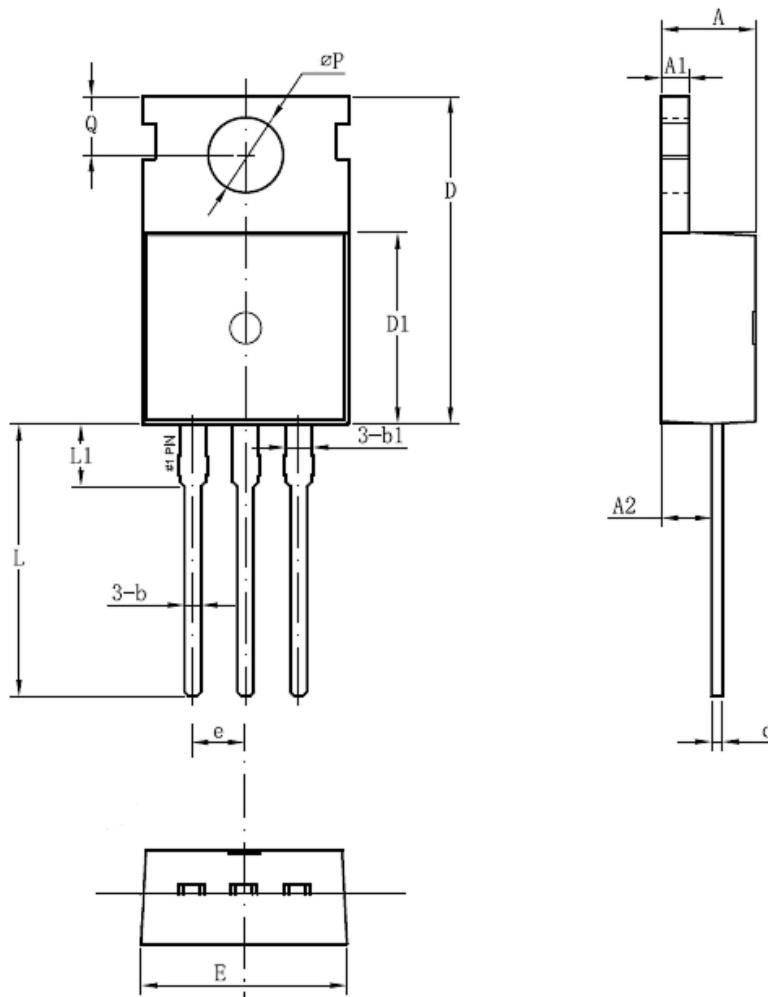
Mechanical Dimensions

TO-263-2

Unit: mm



Symbol	Dimensions(mm)		
	Min.	Typ.	Max.
A	4.30	4.60	4.85
A1	0.00	0.10	0.25
A2	2.59	2.69	2.89
b	0.70	0.81	0.96
b1	-	1.27	-
c	0.36	0.40	0.61
c1	1.15	1.27	1.40
D	8.55	-	9.40
D1	6.40	-	-
E	9.80	10.10	10.31
E1	7.60	-	-
E2	9.80	10.00	10.20
e	2.54(BSC)		
H	14.70	15.20	16.00
L	2.00	2.30	2.84
L1	1.00	1.27	1.40
L2	-	-	2.20
L3	-	0.25	-
θ	0°	-	8°

**68mΩ, 600V, Super Junction N-Channel Power MOSFET**
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**Mechanical Dimensions (Continued)**
**TO-220C**
**Unit: mm**


Symbol	Dimensions(mm)		
	Min.	Typ.	Max.
A	4.30	4.50	4.70
A1	1.20	1.30	1.40
A2	2.20	2.40	2.60
b	0.70	0.80	0.95
b1	-	1.27	-
c	0.40	0.50	0.65
D	15.20	15.70	16.20
D1	9.00	9.20	9.40
E	9.70	10.00	10.20
e	2.54(BSC)		
L	12.60	13.08	13.60
L1	-	3.00	-
ΦP	3.50	3.60	3.80
Q	2.60	2.80	3.00



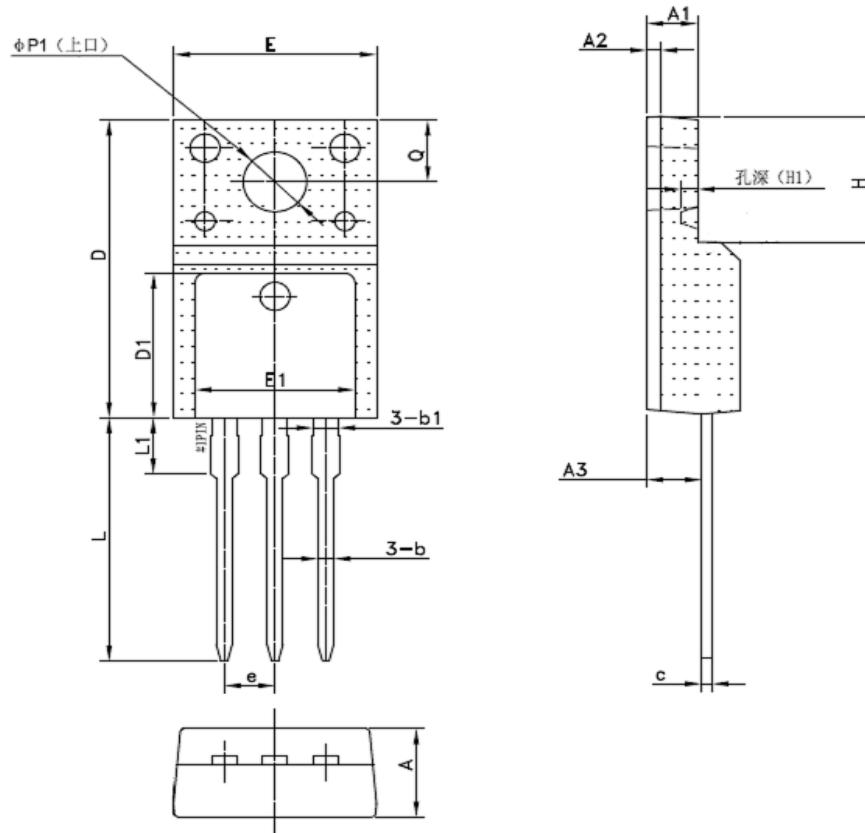
## 68mΩ, 600V, Super Junction N-Channel Power MOSFET

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## Mechanical Dimensions (Continued)

TO-220F

Unit: mm



Symbol	Dimensions(mm)		
	Min.	Typ.	Max.
A	4.30	4.70	4.90
A1	2.34	2.54	2.90
A2	-	0.70	-
A3	2.56	2.76	2.96
b	0.55	-	0.95
b1	-	1.28	-
c	0.42	0.50	0.70
D	14.70	-	16.07
D1	-	7.70	-
E	9.96	10.16	10.36
E1	-	8.00	-
e	2.54(BSC)		
H	-	6.70	-
(H1)	-	(0.81)	-
L	12.48	12.98	13.50
L1	-	2.93	-
ΦP1	-	3.18	-
Q	2.90	3.30	3.50



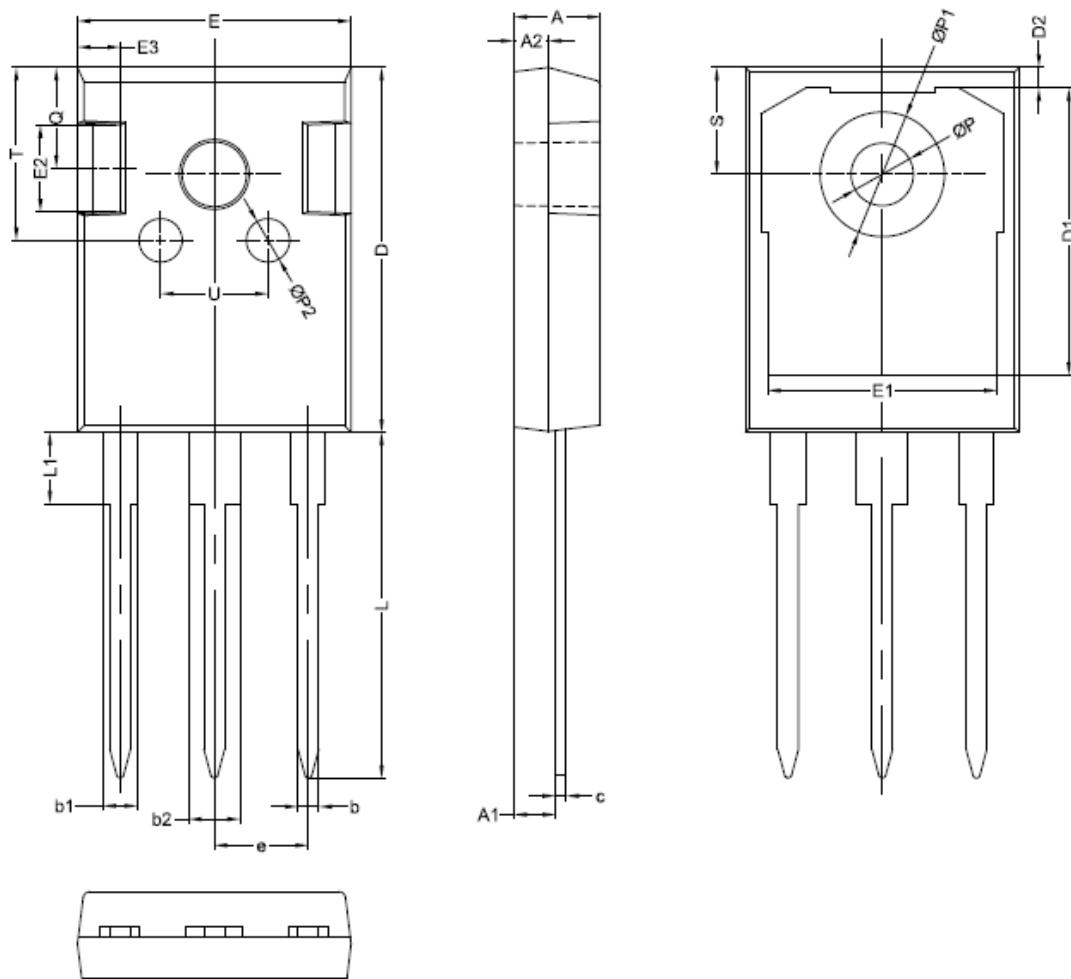
68mΩ, 600V, Super Junction N-Channel Power MOSFET

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## Mechanical Dimensions (Continued)

TO-247

Unit: mm

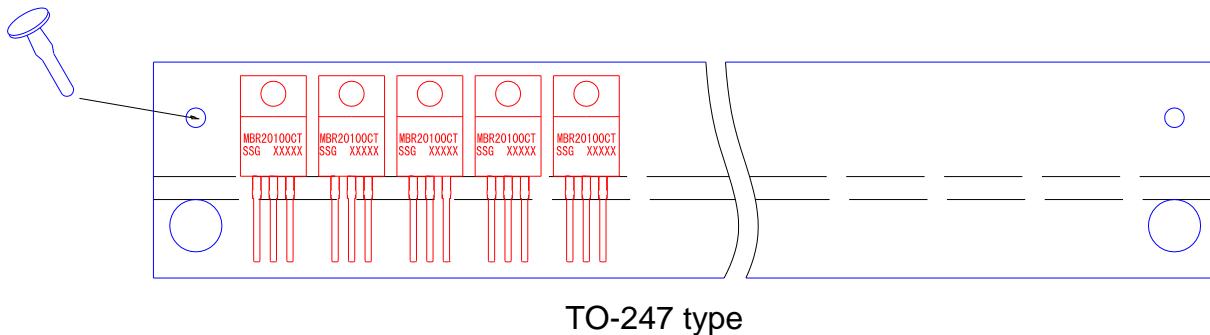


Symbol	Dimensions(mm)			Symbol	Dimensions(mm)		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.80	5.00	5.20	E2	-	5.00	-
A1	2.21	2.41	2.61	E3	-	2.50	-
A2	1.90	2.00	2.10	e	5.44(BSC)		
b	1.10	1.20	1.35	L	19.42	19.92	20.42
b1	-	2.00	-	L1	-	4.13	-
b2	-	3.00	-	P	3.50	3.60	3.70
c	0.55	0.60	0.75	P1	-	-	7.40
D	20.80	21.00	21.20	P2	-	2.50	-
D1	-	16.55	-	Q	-	5.80	-
D2	-	1.20	-	S	6.05	6.15	6.25
E	15.60	15.80	16.00	T	-	10.00	-
E1	-	13.30	-	U	-	6.20	-

68mΩ, 600V, Super Junction N-Channel Power MOSFET

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## Package Information

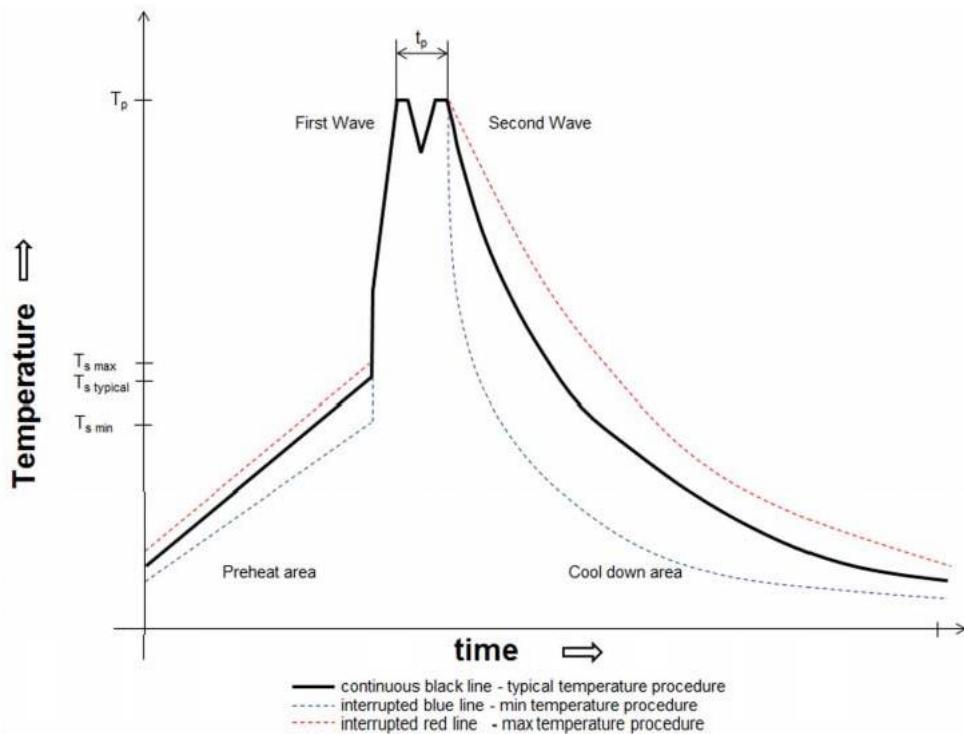


Package Type	Packing Information
	pcs / Line
TO-247	30



## Wave Solder Information

### Classification Wave Soldering Profile:



### Classification Wave Profile

Profile Feature	Pb-Free Assembly	Sn-Pb Assembly
Preheat		
- Temperature Min ( $T_{smin}$ )	100°C	100°C
- Temperature Typical ( $T_{stypical}$ )	120°C	120°C
- Temperature Max ( $T_{smax}$ )	130°C	130°C
- Time ( $t_s$ ) from ( $T_{smin}$ to $T_{smax}$ )	70 seconds	70 seconds
△ preheat to max Temperature	150°C max.	150°C max.
Peak temperature ( $T_p$ )	250°C - 260°C	235°C - 260°C
Time of actual peak temperature ( $t_p$ )	max. 10 seconds max. 5 second each wave	max. 10 seconds max. 5 second each wave
Ramp-down rate		
- Min	~ 2 K/s	~ 2 K/s
- Typical	~ 3.5 K/s	~ 3.5 K/s
- Max	~ 5 K/s	~ 5 K/s
Time 25°C to 25°C	4 minutes	4 minutes

refer to EN 61760-1:2006



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